

PLD Connections

Signal Name	Pin	Connector	I/O	Signal Name	Pin	Connector	I/O	Signal Name	Pin	Connector	I/O
Addr00	58	J2-30	O	Signal3	2	J1-18	I	TrigLatch-L	28	J1-02	O
Addr01	57	J2-29	O	Signal4	4	J1-17	I	SampleClkEna-L	27	J1-03	O
Addr02	56	J2-28	O	Signal5	5	J1-16	I	PreTrigDone-L	24	J1-04	O
Addr03	55	J2-27	O	Signal6	8	J1-15	I	DB0	22	J1-05	I/O
Addr04	54	J2-26	O	Signal7	9	J1-14	I	DB1	21	J1-06	I/O
Addr05	51	J2-25	O	DB7	10	J1-12	I/O	DB2	20	J1-07	I/O
Addr06	50	J2-24	O	DB6	11	J1-11	I/O	DB3	17	J1-08	I/O
Addr07	49	J2-23	O	TDI	14	J3-09		DB4	16	J1-09	I/O
Addr08	46	J2-22	O	DB5	15	J1-10	I/O	DB5	15	J1-10	I/O
Addr09	45	J2-21	O	DB4	16	J1-09	I/O	DB6	11	J1-11	I/O
Addr10	40	J2-20	O	DB3	17	J1-08	I/O	DB7	10	J1-12	I/O
Addr11	37	J2-19	O	DB2	20	J1-07	I/O	Signal7	9	J1-14	I
Addr12	36	J2-18	O	DB1	21	J1-06	I/O	Signal6	8	J1-15	I
Addr13	35	J2-17	O	DB0	22	J1-05	I/O	Signal5	5	J1-16	I
Addr14	34	J2-16	O	TMS	23	J3-05		Signal4	4	J1-17	I
Addr15	33	J2-15	O	PreTrigDone-L	24	J1-04	O	Signal3	2	J1-18	I
Addr16	30	J2-14	O	SampleClkEna-L	27	J1-03	O	Signal2	81	J1-19	I
DB0	22	J1-05	I/O	TrigLatch-L	28	J1-02	O	Signal1	80	J1-20	I
DB1	21	J1-06	I/O	SampleClk	29	J2-09	O	Signal0	79	J1-21	I
DB2	20	J1-07	I/O	Addr16	30	J2-14	O	TrigChEna-PL	76	J1-23	I
DB3	17	J1-08	I/O	Addr15	33	J2-15	O	TrigChState-PL	74	J1-24	I
DB4	16	J1-09	I/O	Addr14	34	J2-16	O	ReadLoAddr	73	J1-27	I
DB5	15	J1-10	I/O	Addr13	35	J2-17	O	ReadHiAddr	70	J1-28	I
DB6	11	J1-11	I/O	Addr12	36	J2-18	O	TimeBaseSel0	69	J1-29	I
DB7	10	J1-12	I/O	Addr11	37	J2-19	O	TimeBaseSel1	68	J1-30	I
Oscillator	75		I	Addr10	40	J2-20	O	TimeBaseSel2	67	J1-31	I
PreTrigDone-L	24	J1-04	O	Addr09	45	J2-21	O	SysReset-PL	64	J1-33	I
ReadHiAddr	70	J1-28	I	Addr08	46	J2-22	O	SysEna-PL	63	J1-34	I
ReadLoAddr	73	J1-27	I	Addr07	49	J2-23	O	SampleFreq0	61	J1-35	I
SampleClk	29	J2-09	O	Addr06	50	J2-24	O	SampleClk	29	J2-09	O
SampleClkEna-H	60		O	Addr05	51	J2-25	O	Addr16	30	J2-14	O
SampleClkEna-L	27	J1-03	O	Addr04	54	J2-26	O	Addr15	33	J2-15	O
SampleFreq0	61	J1-35	I	Addr03	55	J2-27	O	Addr14	34	J2-16	O
Signal0	79	J1-21	I	Addr02	56	J2-28	O	Addr13	35	J2-17	O
Signal1	80	J1-20	I	Addr01	57	J2-29	O	Addr12	36	J2-18	O
Signal2	81	J1-19	I	Addr00	58	J2-30	O	Addr11	37	J2-19	O
Signal3	2	J1-18	I	SampleClkEna-H	60		O	Addr10	40	J2-20	O
Signal4	4	J1-17	I	SampleFreq0	61	J1-35	I	Addr09	45	J2-21	O
Signal5	5	J1-16	I	TCK	62	J3-01		Addr08	46	J2-22	O
Signal6	8	J1-15	I	SysEna-PL	63	J1-34	I	Addr07	49	J2-23	O
Signal7	9	J1-14	I	SysReset-PL	64	J1-33	I	Addr06	50	J2-24	O
SysEna-PL	63	J1-34	I	TimeBaseSel2	67	J1-31	I	Addr05	51	J2-25	O
SysReset-PL	64	J1-33	I	TimeBaseSel1	68	J1-30	I	Addr04	54	J2-26	O
TCK	62	J3-01		TimeBaseSel0	69	J1-29	I	Addr03	55	J2-27	O
TDI	14	J3-09		ReadHiAddr	70	J1-28	I	Addr02	56	J2-28	O
TDO	71	J3-03		TDO	71	J3-03		Addr01	57	J2-29	O
TimeBaseSel0	69	J1-29	I	ReadLoAddr	73	J1-27	I	Addr00	58	J2-30	O
TimeBaseSel1	68	J1-30	I	TrigChState-PL	74	J1-24	I	TCK	62	J3-01	
TimeBaseSel2	67	J1-31	I	Oscillator	75		I	TDO	71	J3-03	
TMS	23	J3-05		TrigChEna-PL	76	J1-23	I	TMS	23	J3-05	
TrigChEna-PL	76	J1-23	I	Signal0	79	J1-21	I	TDI	14	J3-09	
TrigChState-PL	74	J1-24	I	Signal1	80	J1-20	I	SampleClkEna-H	60		O
TrigLatch-L	28	J1-02	O	Signal2	81	J1-19	I	Oscillator	75		I

VCC: 3, 13, 26, 38, 43, 53, 66, 78

GND: 7, 19, 32, 42, 47, 59, 72, 82